Dec. 2006

Section - A (Marks : 2 each)

1 (a) What is super pipelining ?

(b) Explain about RISC processors.

(c) Explain about main memory.

(d) Explain about I/O modes.

(e) What is meant by DMA ?

(f) What is cache memory ?

(g) Explain about parallel interfaces taking example 8255.

(h) Explain microprogrammed control.

(i) Explain pipelining in CPU design.

(j) Explain about the computer organization.

Section - B (Marks : 5 each)

Q. 2 What is meant by superscalar processor ? Explain the concept of pipelining in superscalar processor ?

Q. 3 What are the advantages you got with virtual memory ?

Q. 4 Write about DMA transfer ?

Q. 5 Explain about I/O processor.

Q. 6 Explain about parallel and distributed computers.

Section - C (Marks : 10 each)

Q. 7 What is memory interleaving ? How is it different from Cache memory ?

Q. 8 Write an Algorithm of multiplication ?

Q. 9 Explain about the multiprocessors ?