May 2006

Section - A (Marks : 2 each)

Q 1 (a) Write any six characteristics of RISC.

(b) Write about DMA Transfer.

(c) Explain about Interleave Memory.

(d) What is channel ?

(e) Explain about I/O processor.

(f) Compare and contrast characteristic features of different memories.

(g) Give Machine Architecture of 8086.

(h) Explain about Parallel Computers.

(i) Explain about MIMD Machines.

(j) Explain about I/O Modes.

Section - B (Marks : 5 each)

Q. 2 Compare the instruction set Architecture in RISC and CISC prosessors in terms of instruction formats. addressing modes and cycles per instruction (CPI).

Q. 3 What is memory organisation ? Explain various memories.

Q. 4 What cause a processor pipeline to be underpipelined ?

Q. 5 What is meant by hierarchical bus system for multiprocessing systems ?

Q. 6 Compare interrupt I/O control with DMA I/O.

Section - C (Marks : 10 each)

Q. 7 Write an Algorithm of Addition.

Q. 8 Explain about transaction processing benchmarks.

Q. 9 Explain about parallel and distributed Computers.