

Roll No. 

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**Total No. of Pages: 01**  
**Total No. of Questions: 07**

**BCA (Sem.-2<sup>nd</sup>)**  
**COMPUTER SYSTEM ARCHITECTURE**  
**Subject Code: BSBC-204**  
**Paper ID: [B1116]**

**Time: 3 Hrs.**

**Max. Marks: 60**

**INSTRUCTIONS TO CANDIDATE:**

- 1. Section-A is compulsory.*
- 2. Section-B Attempt any four questions.*

**SECTION-A**

**10x2=20**

**Q. 1.**

- (a) Define parallel processing?
- (b) Define RTL?
- (c) Give example of two address instruction?
- (d) Name some shift micro operation?
- (e) Define Interrupt?
- (f) What is register set?
- (g) Define DMA?
- (h) How hit ratio is measured?
- (i) Define BUS?
- (j) What is register stack?

**Section-B**

**4x10=40**

- Q. 2.** Explain Von Neumann Architecture?
- Q. 3.** Discuss various types of arithmetic and shift micro operations?
- Q. 4.** Discuss the various types of address instructions?
- Q. 5.** Explain the working of 16 bit common bus system?
- Q. 6.** Explain the working of DMA controller?
- Q. 7.** Discuss memory hierarchy?

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