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Total No. of Pages : 2

Total No. of Questions : 07

BCA (Sem.-2)

## COMPUTER SYSTEM ARCHITECTURE

Subject Code : BSBC-204 (2011 Batch)

Paper ID : [B1116]

Time : 3 Hrs.

Max. Marks : 60

### INSTRUCTION TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students has to attempt any FOUR questions.

### SECTION-A

1. Write briefly :

- (a) Define Flynn's classification of computers.
- (b) What is register? List any three 16 bit registers.
- (c) Define instruction. List various types of instruction.
- (d) List various characteristics of hardwired control unit.
- (e) What is shift micro operation? Explain with example.
- (f) What is serial and parallel port? Explain.
- (g) What is memory stack? Explain.
- (h) What is need of I/O interface unit?
- (i) What is associative memory? List its advantages.
- (j) Define LRU in context to cache memory.

## SECTION-B

2. What are various I/O data transfer techniques? Explain each technique with their relative merits and demerits.
3. Write notes on the following :
  - (a) Types of buses.
  - (b) Register Transfer language.
4. What is micro programmed control unit? Draw and explain its working.
5. What is addressing mode? Explain various addressing modes with their advantages and disadvantages.
6. What is cache memory? Explain how data is written into cache. Also discuss the concept of locality of reference in context to Cache memory.
7. Explain the following :
  - (a) Memory hierarchy.
  - (b) Mobile Device Architecture.