Visit www.brpaper.com for

downloading previous year question papers of B-tech, Diploma, BBA, BCA, MBA, MCA, Bsc-IT, Msc-IT, M-Tech, PGDCA, B-com

Roll No.

Total No. of Pages : 02

Total No. of Questions : 09

B.Tech.(CSE/IT) (Sem.-3) COMPUTER ARCHITECTURE Subject Code : CS-201 Paper ID : [A0451]

Time: 3 Hrs.

Max. Marks: 60

INSTRUCTIONS TO CANDIDATES :

- SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks 1. each.
- 2. SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.
- SECTION-C contains THREE questions carrying TEN marks each and students 3. have to attempt any TWO questions.

SECTION-A

- I) Write briefly :
 - a) What is instruction pipelining?
 - b) What do you mean by interleaved memory?
- 31.00 c) What do you mean by Interrupt - initiated I/O Concept?
 - d) Define the terms:-Seek time, Rotational Delay.
 - e) Draw top levelled view of computer components.
 - f) What is the difference between micro program and micro code?
 - g) What do you mean by real time computer?
 - h) Write major requirement for I/O module.
 - i) What is cache memory?
 - i) What is the role of shift register in digital computer?

Visit www.brpaper.com for

downloading previous year question papers of B-tech, Diploma, BBA, BCA, MBA, MCA, Bsc-IT, Msc-IT, M-Tech, PGDCA, B-com

SECTION-B

- 2) What is the difference between I/O mapped input / output and memory mapped input/output? What are the advantages and disadvantages of each?
- 3) Compare and Contrast super pipelined machine and super scalar machines.
- 4) Give an overview of CISC Architecture.
- 5) Write functional view of computer and which are the possible computers operations.
- 6) Give the comparison between and examples of hardwired control unit and micro programmed control unit.

SECTION-C

- 7) Explain in detail the main features of at least two performance evaluation benchmarks.
- 8) a) When a device interrupt occurs, how the processor determines which device issued the interrupt.
 - b) When a DMA module takes control of a bus and while it retains control of the bus, what does the processor do?
- 9) How does pipelining improve performance?