

SECTION-B

- 2) What is the difference between I/O mapped input / output and memory mapped input/output? What are the advantages and disadvantages of each?
- 3) Compare and Contrast super pipelined machine and super scalar machines.
- 4) Give an overview of CISC Architecture.
- 5) Write functional view of computer and which are the possible computers operations.
- 6) Give the comparison between and examples of hardwired control unit and micro programmed control unit.

SECTION-C

- 7) Explain in detail the main features of at least two performance evaluation benchmarks.
- 8)
 - a) When a device interrupt occurs, how the processor determines which device issued the interrupt.
 - b) When a DMA module takes control of a bus and while it retains control of the bus, what does the processor do?
- 9) How does pipelining improve performance?