Roll No. ⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚   **Total No. of Pages : 02**

Total No. of Questions : 07

BCA (2011 & Onward)   (Sem.–2)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BSBC-204
Paper ID : [B1116]

Time : 3 Hrs.   **Max. Marks : 60**

INSTRUCTION TO CANDIDATES :
1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

SECTION-A

1. **Write briefly :**

   a) What is meant by stored program concept?

   b) Define the Register transfer language.

   c) Discuss the concept of Zero address instruction.

   d) Why addressing modes are required?

   e) What is the use of strobe?

   f) How parallel port is used for I/O?

   g) Differentiate ROM from cache memory.

   h) List the features of Android system.

   i) List the different types of shift operators.

   j) What is the role of I/O processor?

SECTION-B

2. What is meant by SISD, SIMD and MIMD architecture? Differentiate SIMD and SISD architectures.

3. Discuss the use of logic and shift micro operations with the help of examples.

4. What are the major design issues of control unit? Discuss the micro prorgrammed approach of control unit design.

5. Explain the following data transfer techniques :

   a) Synchronous

   b) Asynchronous

6. What is the role of input output interface? Discuss isolated input output and memory mapped input output techniques.

7. Write short notes on the following :

   a) Associative Memory

   b) FIFO page replacement algorithm