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B.Tech.(CSE)/(Electronics & Computer Engg.)/ (IT) (2011 Onwards) (Sem.-4)

MICROPROCESSOR & ASSEMBLY LANGUAGE PROGRAMMING

Subject Code : BTCS-404 Paper ID : [A1186]

Time: 3 Hrs. Max. Marks: 60

INSTRUCTION TO CANDIDATES:

- SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
- 2. SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.
- 3. SECTION-C contains THREE questions carrying TEN marks each and students have to attempt any TWO questions.

SECTION-A

I. Write briefly:

- a. Give two examples of branch instructions in 8085.
- b. What operation can be performed by using the instruction XRA A? Specify the status of Z and CY flags.
- c. List four categories of 8085 instructions that manipulate data.
- d. What is DMA?
- e. What are the advantages of an assembly level language in comparison with high level languages?
- f. How many memory locations can be addressed by a microprocessor with 14 address lines?
- g. What is the function of the \overline{WR} signal on the memory chip?
- h. What is a bus?
- i. What is the use of 8255 Programmable Peripheral Interface?
- j. List various addressing modes available in 8085.

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SECTION-B

- II. Explain the control and status signal of 8085.
- III. Specify the contents of the registers and the flag status as the following instructions are executed:

MVI A, 00H

MVI B, F8H

MOV C, A

MOV D, B

HLT

- IV. Explain in detail interfacing of keyboards with 8085 microprocessor.
- V. Write short notes on Motorola 68000.
- VI. Describe the use of 8251 I/O processor.

SECTION-C

- VII. Describe in detail the construction and working of Microprocessor Controlled Temperature System (MCtTS).
- VIII. List and explain the operating modes of the 8255 Programmable Peripheral Interface.
- IX. Describe interrupt initiated and DMA data transfer modes.

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